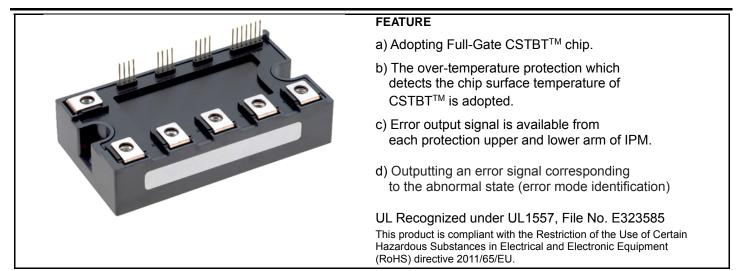


<Intelligent Power Modules>

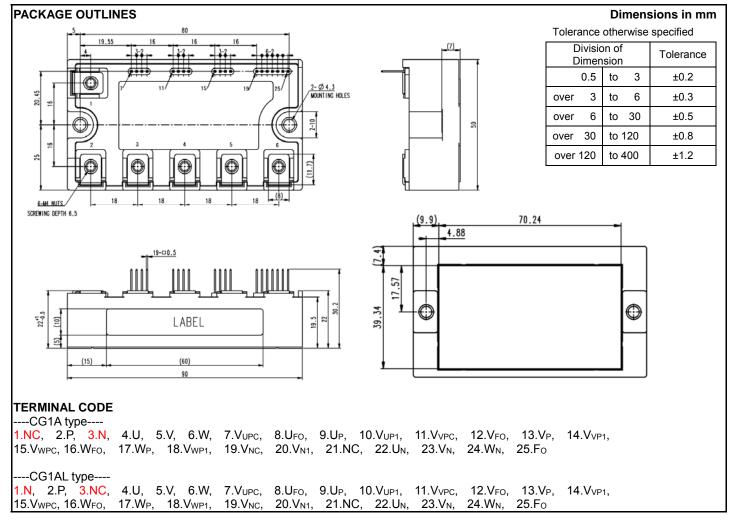
PM35CG1A120/PM35CG1AL120

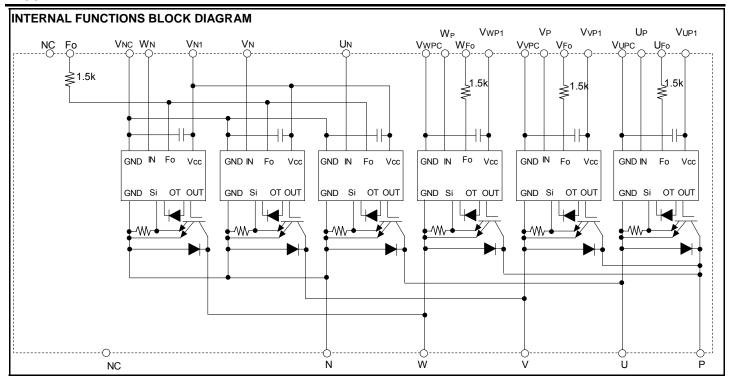
FLAT-BASE TYPE INSULATED PACKAGE



APPLICATION

General purpose inverter, servo drives and other motor controls





MAXIMUM RATINGS (Tvj = 25°C, unless otherwise noted)

INVERTER PART

Symbol	Parameter	Conditions	Ratings	Unit
V _{CES}	Collector-Emitter Voltage	V _D =15 V, V _{CIN} =15 V	1200	V
lc		T _c =25 °C	35	•
I _{CRM}	Collector Current	Pulse	70	A
Ptot	Total Power Dissipation	T _c =25 °C	290	W
l _E	Emitter Current	T _c =25 °C	35	^
I _{ERM}	(Free-wheeling Diode Forward current)	Pulse	70	A
Tvj	Junction Temperature		-20 ~ +150	°C

*: Tc measurement point is just under the chip.

CONTROL PART

Symbol	Parameter	Conditions	Ratings	Unit
VD	Supply Voltage	Applied between: V_{UP1} - V_{UPC} , V_{VP1} - V_{VPC} , V_{WP1} - V_{WPC} , V_{N1} - V_{NC}	20	V
V _{CIN}	Input Voltage	Applied between: U_P-V_{UPC} , V_P-V_{VPC} , W_P-V_{WPC} , U_N , V_N , W_N-V_{NC}	20	V
V _{FO}	Fault Output Supply Voltage	Applied between: U_{FO} - V_{UPC} , V_{FO} - V_{VPC} , W_{FO} - V_{WPC} , FO- V_{NC}	20	V
I _{FO}	Fault Output Current	Sink current at U_{FO} , V_{FO} , W_{FO} , Fo terminals	20	mA

TOTAL SYSTEM

Symbol	Parameter	Conditions	Ratings	Unit
V _{CC(PROT)}	Supply Voltage Protected by SC	V _D =13.5 V∼16.5 V, Inverter Part, Tvj=+125°C start	800	V
T _{stg}	Storage Temperature	-	-40 ~ +125	°C
Tc	Operating Case Temperature	-	-20 ~ +125	°C
Visol	Isolation Voltage	60Hz, Sinusoidal, Charged part to Base plate, AC 1min, RMS	2500	V

*: Tc measurement point is just under the chip.

THERMAL RESISTANCE

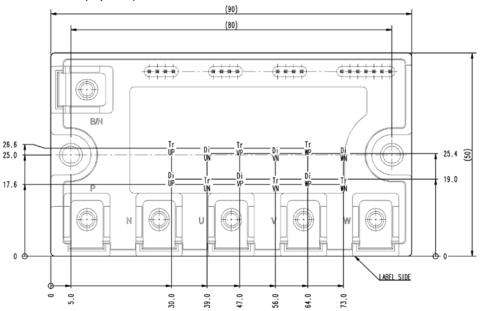
Symbol	Parameter	Conditions	Limits			Unit
			Min.	Тур.	Max.	Unit
R _{th(j-c)Q}	Thermal Resistance	Junction to case, IGBT, per 1 element (Note1)	-	-	0.43	K/W
$R_{th(j-c)D}$		Junction to case, FWD, per 1 element (Note1)	-	-	0.67	
R _{th(c-s)}	Contact Thermal Resistance	Case to heat sink, per 1 module,		19.1	-	K/kW
		Thermal grease applied (Note.1, 2)		19.1		

Note1. If you use this value, $R_{\mbox{th}(s\mbox{-a})}$ should be measured just under the chips.

Note2. Typical value is measured by using thermally conductive grease of λ =0.9W/(m·K), D_(C-S)=50 µm.

CHIP LOCATION (Top view)

Dimension in mm, torelance: ±1mm





ELECTRICAL CHARACTERISTICS (Tvj= 25°C, unless otherwise noted) **INVERTER PART**

Symbol	Parameter	Conditions		Limits			Unit	
Symbol	Parameter	Conditions			Min.	Тур.	Max.	Unit
		V _D =15 V, I _C =35 A Tvj=	T	Terminal	-	-	1.7	
V	Collector-Emitter Saturation Voltage		Tvj=25 °C	Chip	-	1.3	-	v
V _{CEsat}	-	$\lambda = 0 \lambda $ Dulaced (Fig. 1)	Tvj=125 °C	Terminal	-	-	1.95	v
		V _{CIN} =0 V, Pulsed, (Fig.1)	TVJ=125 C	Chip	-	1.5	-	
	Emitter-Collector Voltage	V _D =15 V, I _E =35 A, Tvj=25	Tvi=25 °C	Terminal		-	2.35	- V
V _{EC}			10]=23 0	Chip	-	1.75	-	
V EC		V _{CIN} = 15 V, pulsed, (Fig.2) Tvj=125 °C	Tyi=125 °C	Terminal	-	-	2.6	
			Chip	-	1.95	-		
t _{on}		$V_D=15 V$, $V_{CIN}=0 V \leftrightarrow 15 V$, $V_{CC}=600 V$, $I_C=35A$, Tvj=125 °C, Inductive Load (Fig.3, 4)		0.3	0.7	1.2		
t _{rr}				-	0.13	0.4		
t _{c(on)}	Switching Time				-	0.2	0.4	μs
t _{off}					-	1.0	2.8	
t _{c(off)}					-	0.4	1.2	
	Collector Emitter Out off Ourset	$V_{CE}=V_{CES}, V_{D}=15 V,$		Tvj=25 °C	-	-	1	m 4
I _{CES}	Collector-Emitter Cut-off Current	V _{CIN} =15 V (Fig.5)		Tvj=125 °C	-	-	10	mA

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (Tvj = 25°C, unless otherwise noted)

CONTROL PART

Symbol	Parameter Conditions			Limits		Unit	
Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
			V_{P1} - V_{PC}	-	4	6	
1		V_{D} =15 V, V_{CIN} =15 V	V _{N1} -V _{NC}	-	12	18	
ID	Circuit Current	V_D =15 V, V_{CIN} =0 V \leftrightarrow 15 V, V_{CC} =800 V	V _{P1} -V _{PC}	-	13	15	mA
		l _c =0A, Tvj=125 °C, f _c ≤20kHz	V _{N1} -V _{NC}	-	38	45]
V _{th(ON)}	Input ON Threshold Voltage	Applied between:		1.2	1.5	1.8	v
$V_{th(OFF)}$	Input OFF Threshold Voltage	U_{P} - V_{UPC} , V_{P} - V_{VPC} , W_{P} - V_{WPC} , U_{N} , V_{N} , W_{N} - V_{NC}		1.7	2.0	2.3	v
SC	Short Circuit Trip Level	-20≤Tvj≤125 °C, V _D =15 V (Fig.3, 6)		70	-	-	А
t _{d(SC)}	Short Circuit Current Delay Time	V _D =15 V, Tvj=125 °C (Fig.3, 6)		-	2.0	-	μs
ОТ	O	Datast temporature of ICPT ship surface	Trip level	150	-	-	0°
OT _(hys)	Over Temperature Protection	Detect temperature of IGBT chip surface	Hysteresis	-	20	-	
UVt	Supply Circuit		Trip level	11.0	12.0	12.7	v
UVr	Under-Voltage Protection	-	Reset level	-	12.5	-	
I _{FO(H)}	Fault Output Current		-	-	0.01	mA	
I _{FO(L)}		V _D =15 V, V _{FO} =15 V (Note3)		-	10	15	111A
		V _D =15 V (Note3)	ОТ	-	8.0	-	
t _{FO}	Fault Output Pulse Width		UV	-	4.0	-	ms
			SC	-	2.0	-	

Note3. Fault output is given only when the internal SC, OT & UV protections schemes of either upper or lower arm device operate to protect it.

MECHANICAL RATINGS AND CHARACTERISTICS

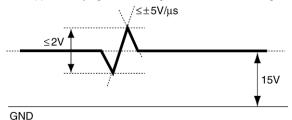
Symbol P	Parameter	Conditions		Limits		
	Falameter	Conditions	Min.	Тур.	Max.	Unit
Ms	Mounting Torque	Mounting part screw : M4	1.5	1.7	2.0	N•m
Mt	Mounting Torque	Main terminal part screw : M4	1.5	1.7	2.0	IN*111
m	mass	-	-	175	-	g

RECOMMENDED CONDITIONS FOR USE

Symbol	Parameter	Conditions	Recommended value	Unit
V _{cc}	Supply Voltage	Applied across P-N terminals	≤ 800	V
V _D	Control Supply Voltage	Applied between : V _{UP1} -V _{UPC} , V _{VP1} -V _{VPC} , V _{WP1} -V _{WPC} ,V _{N1} -V _{NC} (Note4)	15.0±1.5	V
V _{CIN(ON)}	Input ON Voltage	Applied between :	≤ 0.8	V
V _{CIN(OFF)}	Input OFF Voltage	$U_{P}\text{-}V_{UPC}, V_{P}\text{-}V_{VPC}, W_{P}\text{-}V_{WPC}, U_{N}, V_{N}, W_{N}\text{-}V_{NC}$	≥ 9.0	v
f _{PWM}	PWM Input Frequency	Using Application Circuit of Fig. 8	≤ 20	kHz
t _{dead}	Arm Shoot-through Blocking Time	For IPM's each input signals (Fig.7)	≥ 2.5	μs

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note4. With ripple satisfying the following conditions: dv/dt swing $\leq \pm 5$ V/µs, Variation ≤ 2 V peak to peak



INSULATED TYPE

PRECAUTIONS FOR TESTING

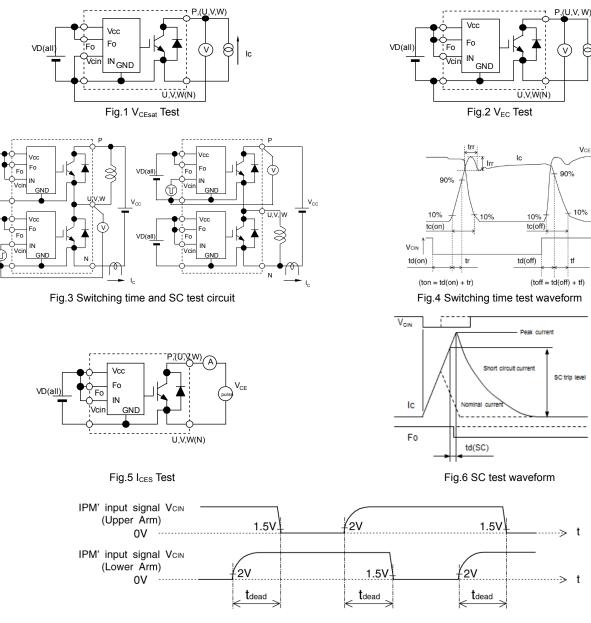
VD(all

1. Before applying any control supply voltage (V_D), the input terminals should be pulled up by resistors, etc. to their corresponding supply voltage and each input signal should be kept off state.

After this, the specified ON and OFF level setting for each input signal should be done.

2. When performing "SC" tests, the turn-off surge voltage spike at the corresponding protection operation should not be allowed to rise above VCES rating of the device.

(These test should not be done by using a curve tracer or its equivalent.)



1.5V: Input on threshold voltage Vth(on) typical value, 2V: Input off threshold voltage Vth(off) typical value Fig. 7 Dead time measurement point example

VCE

10%

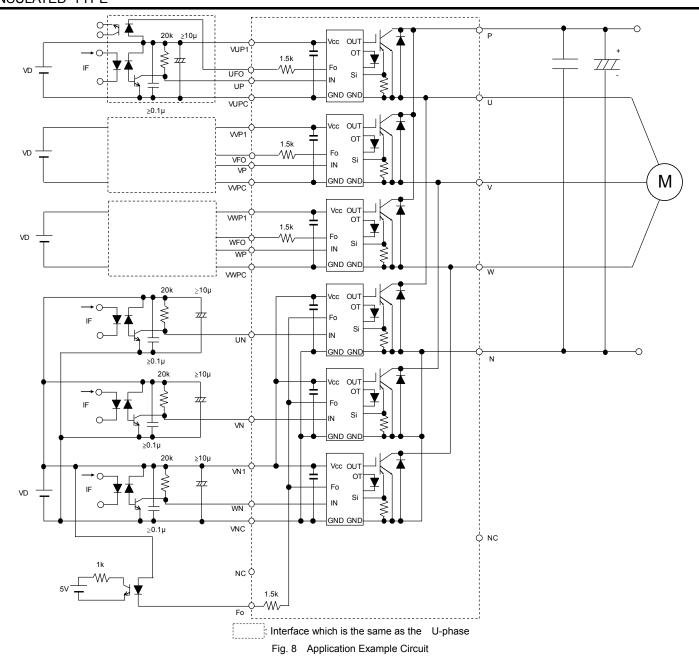
tf

SC trip leve

t

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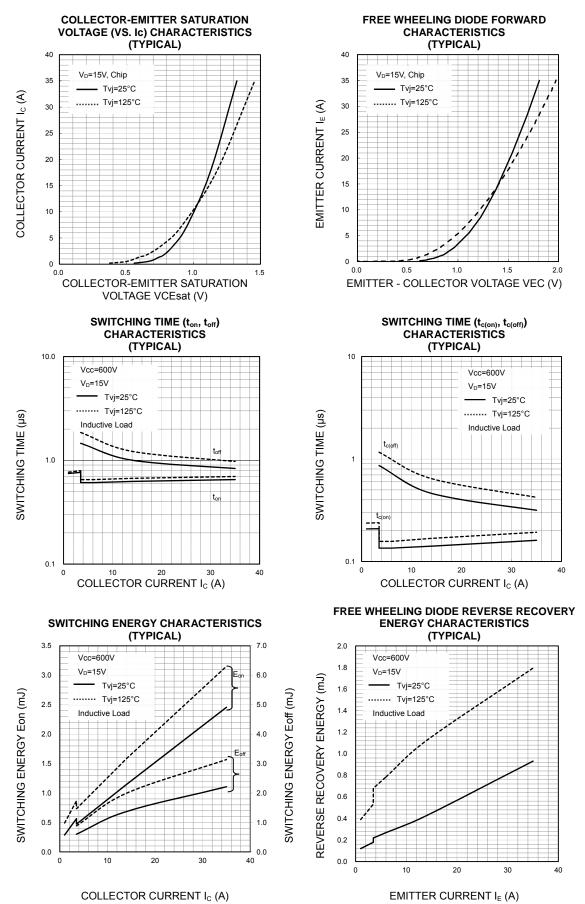
90%

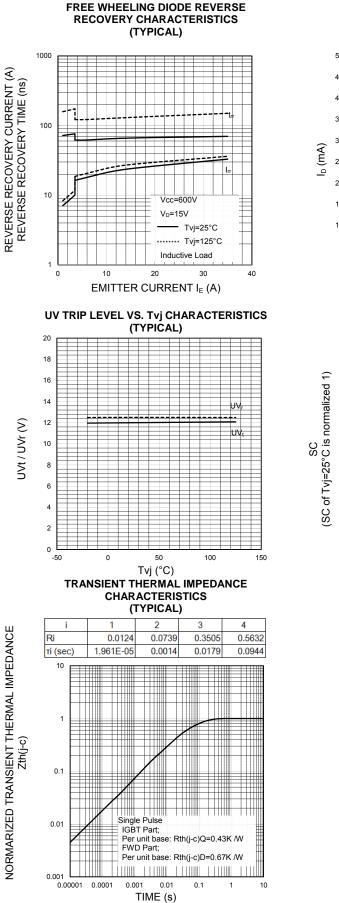


NOTES FOR STABLE AND SAFE OPERATION ;

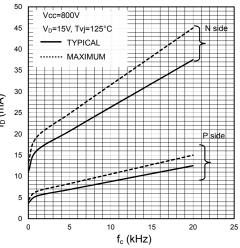
- Design the PCB pattern to minimize wiring length between opto-coupler and IPM's input terminal, and also to minimize the stray capacity between the input and output wirings of opto-coupler.
- Connect low impedance capacitor between the Vcc and GND terminal of each fast switching opto-coupler.
- Fast switching opto-couplers: t_{PLH} , $t_{PHL} \le 0.8 \mu s$, Use High CMR type.
- Slow switching opto-coupler: CTR > 100%
- Use 4 isolated control power supplies (V_D). Also, care should be taken to minimize the instantaneous voltage charge of the power supply.
- Make inductance of DC bus line as small as possible, and minimize surge voltage using snubber capacitor between P and N terminal.

PERFORMANCE CURVES

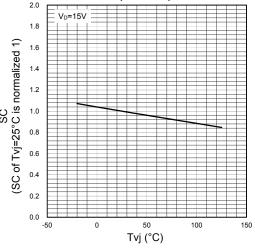








SC TRIP LEVEL VS. Tvj CHARACTERISTICS (TYPICAL)



Keep safety first in your circuit designs!

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